



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Applicant : Alex Kalnitsky, et al Docket No : 93-C-32
Serial No : 08/163,043 Group : 1109
Filed : December 6, 1993 Examiner : T. Dang
For : Enhanced Planarization Technique for an Integrated Circuit

TRANSMITTAL LETTER

Hon. Commissioner of Patents
and Trademarks
Washington, D.C. 20231

Sir:

Enclosed herewith is the Notice of Allowance and Issue Fee Due Form PTOL-85 (Part B and Part C) along with our firm check in the amount of \$1,240.00 for the Issue Fee and an advance order of ten (10) copies in the above-identified application.

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 19-1353. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Lisa K. Jorgenson
Reg. No. 34,845
Attorney for Applicant

SGS-Thomson Microelectronics, Inc.
1310 Electronics Drive/MS 2346
Carrollton, TX 75006
214-466-7414

CERTIFICATE OF MAILING

37 CFR 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Box Issue Fee, Commissioner of Patents and Trademarks, Washington, D.C. 20231 on the date below:

April 11, 1995
Date

Ange Rodriguez
Signature

